

Objectives of the Programme

A field-programmable gate array (FPGA) is an integrated circuit designed to be configured by a customer or a designer after manufacturing—hence "field-programmable". The FPGA configuration is generally specified using a hardware description language (HDL) like VHDL or verilog. The programme is intended to provide preliminary ideas about HDL programming, and also how this can be used to configure an FPGA for power electronic applications. Programme includes design steps of PCB for Power Electronics.

Programme Content

- VHDL / Verilog programming
- Schematic & PCB Design using ORCAD
- Architecture of FPGA
- Power Electronics Applications
- Hands on training
- Implementation issues of Multi level inverter and FPGA solution

Methodology

Following training methods will be applied to improve the effectiveness of the training programme.

- Presentations followed by discussions
- Demonstrations
- Hands-on training
- Tutorials

The training programme will be of in-house and interactive nature and the sessions may extend beyond office hours.

Eligibility

This training programme is intended for teaching staff in Engineering/Technical institutions.

How to Apply

Duly filled in applications in the prescribed format, sponsored by the competent authority may be sent to the Co-ordinator so as to reach on or before February 15. Outstation participants may also send an advance scanned copy of the application through e-mail.

Certificate of Participation

Certificate of Participation will be issued to all participants on successful completion of the programme.

Registration Fee

There shall be no registration fee for participants from Government and Govt.-controlled institutions. Participants from private self-financing institutions are required to pay a registration fee of Rs.500/- for attending the training programme.

Accommodation

For outstation candidates, accommodation can be arranged on request.

Resource Persons

Sessions of the training programme will be handled by faculties from IISc and other institution



Government Engineering College, Idukki

STTP
on
*FPGA and PCB Design for Power
Electronics Applications*

25-2-2014 to 1-3-2014

Application Form

Name :

Age in years :

Designation :

Department :

College :

Address for
Communication :

Phone: Office :

Residence :

Mobile :

Email:

- Total working experience:years
- Whether accommodation required: Yes/No
- Whether attended similar training programme:
Yes/No

The information given above is true to the best of my knowledge and belief. If selected, I will attend the course for the entire duration.

Signature of the candidate

Date:

Sponsorship

Mr./Ms.....
is hereby sponsored to attend the Training Programme on **FPGA And PCB Design For Power Electronics Applications**, conducted at Government Engineering College, Idukki. If selected, he/she will be permitted to attend the programme for the entire duration.

Signature of the Head of Institution Office seal
Designation: Date:

Important dates

Last date for receiving applications
15 February, 2014

Course duration
25-2-2014 to 1-3-2014

Address for Communication

Mr. Sunil Kumar P R/Mr. Anil Kumar V M
Dept. of Electrical & Electronics Engg.
Government Engineering College
Painavu, Idukki – 685 603

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www.gecidukki.ac.in

About the College

Government Engineering College Idukki (GECI), one among the prestigious technological institutions under the Technical Education Department of Government of Kerala, is located at Painavu, Idukki. The institute is affiliated to Mahatma Gandhi University. The college offers 5 undergraduate programmes and 4 post-graduate programmes in engineering. The college is funded under the TEQIP-Phase II program, sanctioned by Ministry of Human Resource Development, Government of India. The college has state-of-the-art laboratory facilities, library with more than 10000 volumes, ICT-enabled class rooms, electronic subscription of journals, to name a few. The misty environs of the campus surrounded by the Idukki wildlife sanctuary and refreshing mountains adds to the beauty and makes it ideal for the pursuit of higher education. The Idukki Dam is just 7 km from the campus

Idukki – “The Eco Friendly Power House of Kerala”

Idukki is the green paradise nestled among the mountains of the Western Ghats. The mountain district is the most picturesque in the state by the green glaze of alluring vegetation and pleasant climate. The place is also a well-known hill station with many tourist attractions like the Munnar Hill Station, Thekkady Boat landing, Periyar Wild Life Sanctuary, Idukki Arch Dam and Waterfalls. Clean and pristine, enchanting and hypnotizing, the land is filled with history and culture.



Short Term Training Programme

on

FPGA and PCB Design for Power Electronics Applications

25-2-2014 to 1-3-2014

Sponsored by

TEQIP Phase II



Co-ordinators

**Mr. Sunil Kumar P R
Mr. Anil Kumar V M**

Organised by

**Dept. of Electrical & Electronics Engg.
Government Engineering College
Painavu, Idukki – 685 603**